



2631/18

PTO/SB/08A (08-03)

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STATEMENT BY APPLICANT**

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Sheet 1 of 3

Complete if Known

Application Number	09/652,116
Filing Date	08/29/2000
First Named Inventor	James D. Barnette
Art Unit	2631
Examiner Name	Qutbuddin Ghulamali
Attorney Docket Number	J. BARNETTE 2-2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
QG		US- 6,434,233	08/15/2002	Bjarnason, et al.	
QG		US- 5,809,033	09/15/1998	Turner, et al.	
QG		US- 5,604,769	02/18/1997	Wang	
QG		US- 5,512,898	04/30/1996	Norsworthy, et al.	
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QG		US- 5,418,849	05/23/1995	Cannalire, et al.	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
06/08/2004	HVUDHG1	0000029 09652116				
01 FC:1806		180.00 OP				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
QG		Floyd M. Gardner "Interpolation in Digital Modems - Part I: Fundamentals" IEEE Transactoins on Communications, Vol. 41, No. 3, March 1993; pp. 501-507	
QG		Lars Erup, et al. "Interpolation in Digital Modems - Part II: Implementation and Performance" IEEE Transactions on Communications, Vol. 41, No. 6, June 1993; pp. 998-1008	
QG		Mandeep Singh Chadha, et al. U.S. Patent Application Serial No. 09/650,851, filed 08/29/2000 entitled "Filter Circuit for a Bit Pump and Method of Configuring the Same"; pp. 1-57; 7 drawing sheets	
QG		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,854, filed 08/29/2000 entitled "Separation Circuit for an Echo Canceling System and Method of Operating the Same"; pp. 1-67; 6 drawing sheets	
QG		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,853, filed 08/29/2000 entitled "Echo Canceling System for a Bit Pump and Method of Opearting the Same"; pp. 1-60; 5 drawing sheets	
QG		James D. Barnette; U.S. Patent Application Serial No. 09/650,850, filed 08/29/2000 entitled "An Interpolator, a Resampler Employing the Interpolator and Method of Interpolating a Signal Associated Therewith"; pp. 1-78; 6 drawing sheets	

Examiner Signature	<i>[Signature]</i>	Date Considered	12/7/04
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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